

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Christophe J. Chevallier

Examiner:

Luong Nguyen

Serial No.:

09/136680

Group Art Unit:

2612

Filed:

August 19, 1998

Docket:

703.032US1

Title:

CMOS IMAGER WITH INTEGRATED NON-VOLATILE MEMORY

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE **Assistant Commissioner for Patents** P.O.Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

CHRISTOPHE J. CHEVALLIER

By his Representatives,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop RCE, Commissioner of Patents, P.O.Box 1450, Alexandria, VA 22313-1450, on this 25th day of June, 2003

Name